

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	975	(serial or parallel) near5 (physical adj layer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:34
L2	81755	encod\$3/decod\$3 or decod\$3/encod\$3 or (encoder and decoder)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:36
L3	803279	(serial\$/deserial\$ and deserial\$/serial\$) or (encoder and decoder) or (serial\$ and (deserial\$ or parallel)) or (serial/parallel and parallel/serial) or (S/P and P/S)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:42
L4	2407044	(transmit\$4 or transmission or send\$3) and (receiv\$3 or receipt\$3 or driver)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:44
L5	57085	2 and 3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:44
L6	53907	5 and (estimat\$3 or detect\$3 or test\$3 or correct\$3 or calculat\$3 or measur\$5 or comput\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:46
L7	255	1 and 6	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:47

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L8	10934	(375/219 or 375/222 or 375/241 or 375/242 or 341/143 or 710/33 or 710/38 or 710/51 or 710/52).ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:49
L9	1227	6 and 8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:50
L10	5859	6 and (PLL or (phase adj lock\$3 adj loop\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:51
L11	245	9 and (PLL or (phase adj lock\$3 adj loop\$2))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:52
L12	99	1 and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/06 16:53

 PALM INTRANETDay : Saturday
Date: 1/6/2007
Time: 16:01:40**Inventor Name Search Result**

Your Search was:

Last Name = CHIANG

First Name = CHINYI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10429844	7159055	150	05/06/2003	PHYSICAL LAYER APPARATUS COMPLIANT TO SERIAL AND PARALLEL ATA INTERFACES	CHIANG, CHINYI
10626587	Not Issued	71	07/25/2003	Circuit structure and signal encoding method for a serial ATA external physical layer	CHIANG, CHINYI
10638414	7085861	150	08/12/2003	SERIAL ATA CONTROL CIRCUIT FOR AUTOMATICALLY SWITCHING CONNECTION PATH	CHIANG, CHINYI
10983694	Not Issued	30	11/09/2004	Multi-purpose IO system	CHIANG, CHINYI

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="CHIANG"/>	<input type="text" value="CHINYI"/>	<input type="button" value="Search"/>

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